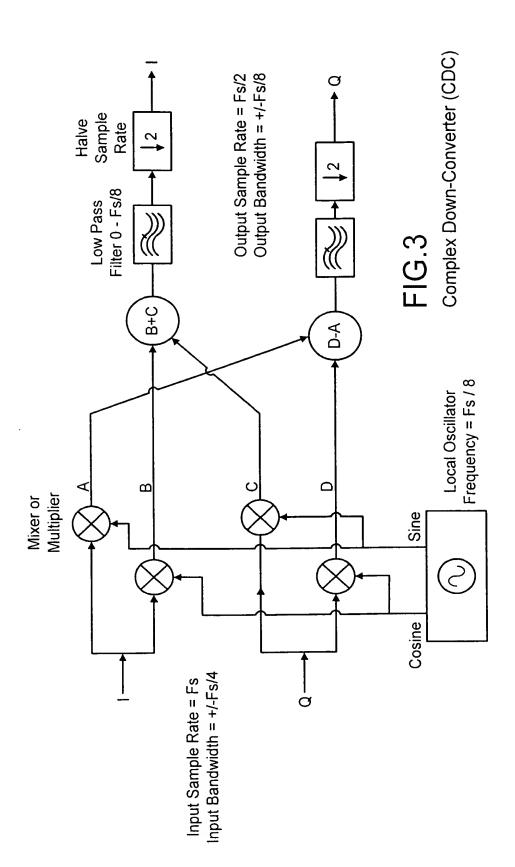
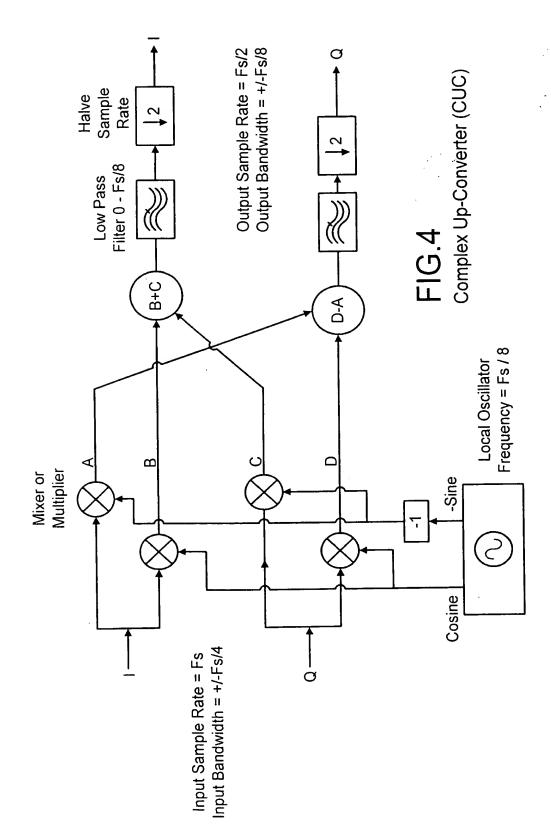


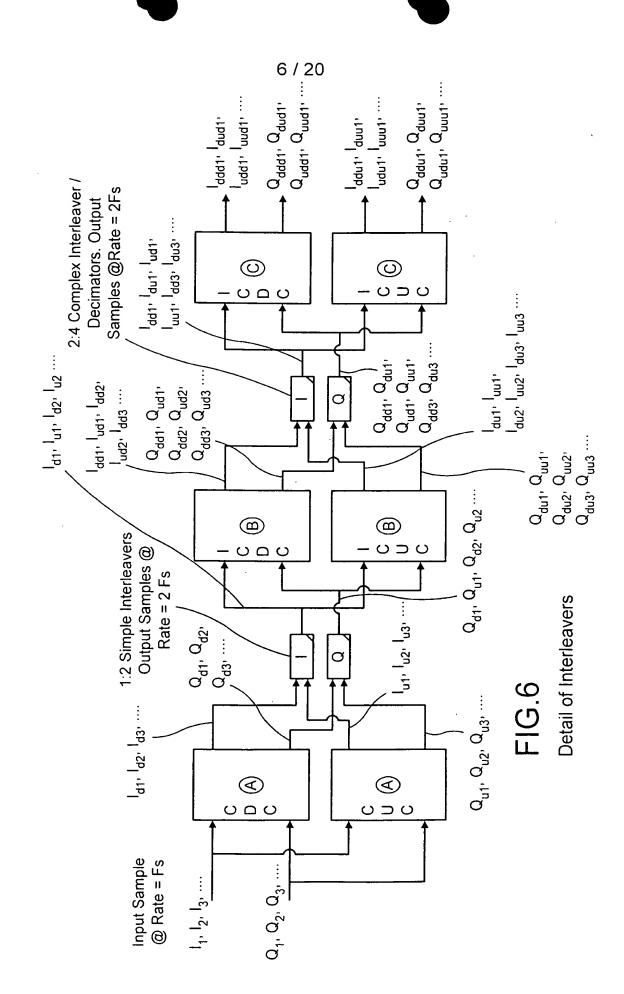
Frequency Band Splitting

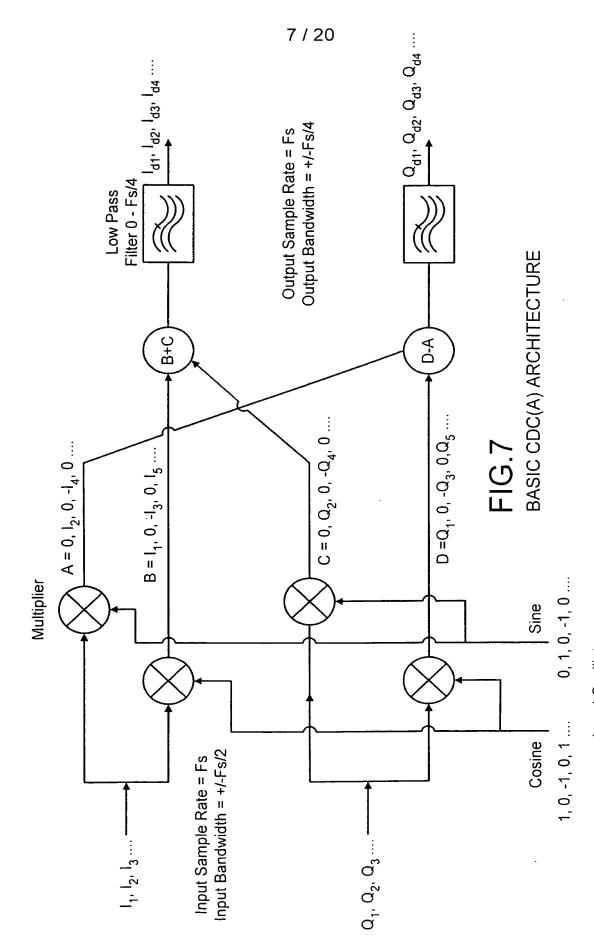


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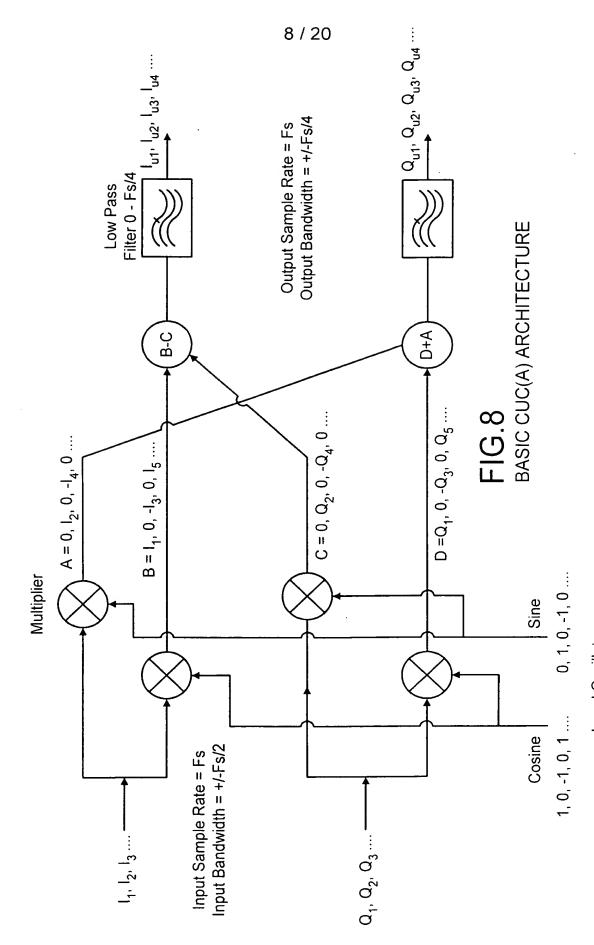


Block Diagram of Interleaved System

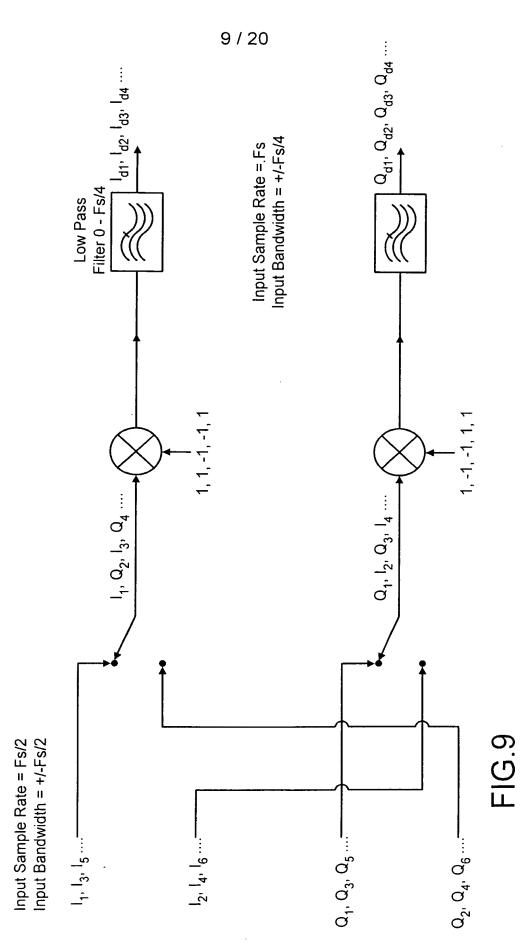




Local Oscillator Frequency = Fs/4

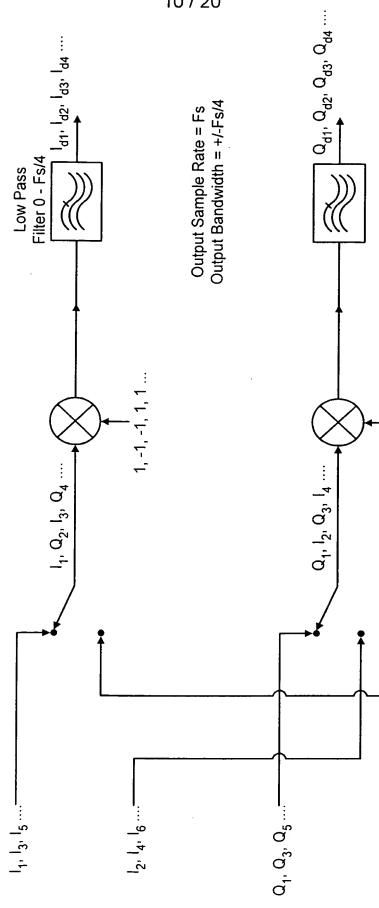


Local Oscillator Frequency = Fs/4



MODIFIED CDC(A) ARCHITECTURE

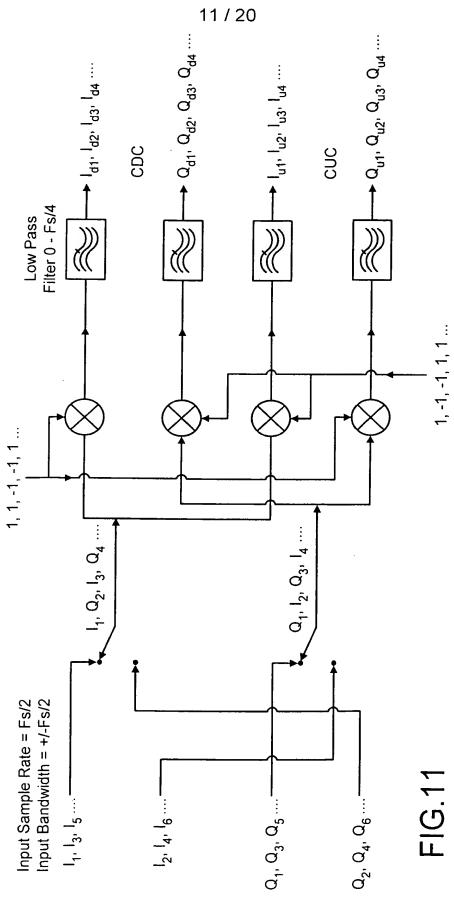
Input Sample Rate = Fs/2 Input Bandwidth = +/-Fs/2



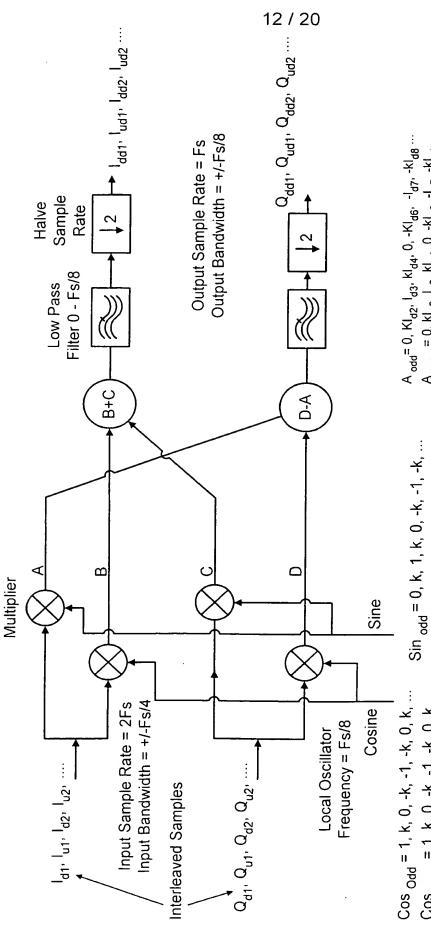
MODIFIED CUC(A) ARCHITECTURE

FIG. 10

02, 04, 06 ...-



COMBINED CDC(A) & CUC(A) ARCHITECTURE



 $A_{odd} = 0$, Kl_{d2} , l_{d3} , kl_{d4} , 0, $-Kl_{d6}$, $-l_{d7}$, $-kl_{d8}$... $A_{even} = 0$, kl_{u2} , l_{u3} , kl_{u4} , 0, $-kl_{u6}$, $-l_{u7}$, $-kl_{u8}$...

Sin _{even} = 0, k, 1, k, 0, -k, 1, -k, ...

Cos _{even} = 1, k, 0, -k, -1, -k, 0, k, ...

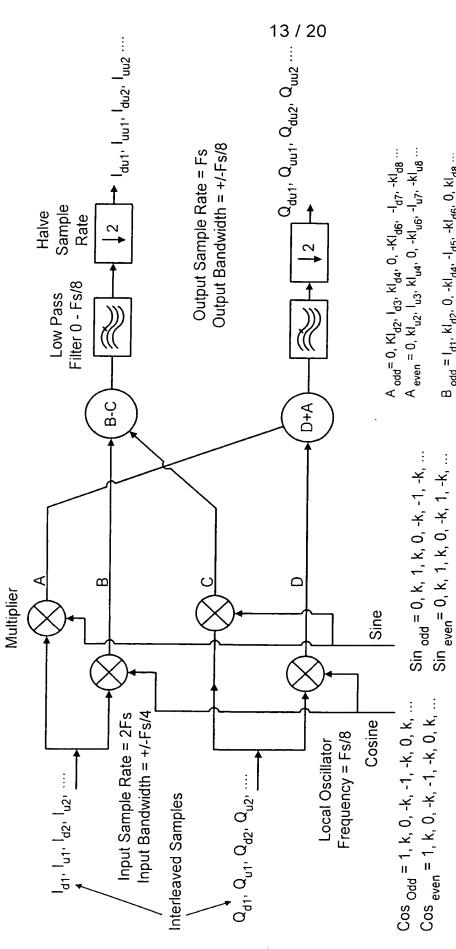
FIG. 12

 $B_{odd} = |_{d1}, k|_{d2}, 0, -k|_{d4}, -|_{d5}, -k|_{d6}, 0, k|_{d8} \dots$ $B_{even} = |_{u1}, k|_{u2}, 0, -k|_{u4}, -|_{u5}, -k|_{u6}, 0, k|_{u8} \dots$

 $C_{odd} = 0$, kQ_{d2} , Q_{d3} , kQ_{d4} , 0, $-kQ_{d6}$, $-Q_{d7}$, $-kQ_{d8}$... $C_{even} = 0$, kQ_{u2} , Q_{u3} , kQ_{u4} , 0, $-kQ_{u6}$, $-Q_{u7}$, $-kQ_{u8}$...

BASIC ICDC(B) ARCHITECTURE

 $\begin{array}{ll} D_{odd} = Q_{d1}, \, kQ_{d2}, \, 0, \, {}^{+}kQ_{d4}, \, {}^{-}Q_{d5}, \, {}^{+}kQ_{d6}, \, 0, \, kQ_{d8} \, \ldots \\ D_{even} = Q_{u1}, \, kQ_{u2}, \, 0, \, {}^{+}kQ_{u4}, \, {}^{-}Q_{u5}, \, {}^{+}kQ_{u6}, \, 0, \, kQ_{u8} \, \ldots \end{array}$



 $B_{odd} = I_{d1}$, kI_{d2} , 0, $-kI_{d4}$, $-I_{d5}$, $-kI_{d6}$, 0, kI_{d8} ... $B_{even} = I_{u1}$, kI_{u2} , 0, $-kI_{u4}$, $-I_{u5}$, $-kI_{u6}$, 0, kI_{u8} ...

 $C_{odd} = 0$, kQ_{d2} , Q_{d3} , kQ_{d4} , 0, $-kQ_{d6}$, $-Q_{d7}$, $-kQ_{d8}$... $C_{even} = 0$, kQ_{u2} , Q_{u3} , kQ_{u4} , 0, $-kQ_{u6}$, $-Q_{u7}$, $-kQ_{u8}$...

BASIC ICUC(B) ARCHITECTURE

FIG. 13

 $D_{odd} = Q_{d1}, kQ_{d2}, 0, -kQ_{d4}, -Q_{d5}, -kQ_{d6}, 0, kQ_{d8} ...$ $D_{even} = Q_{u1}, kQ_{u2}, 0, -kQ_{u4}, -Q_{u5}, -kQ_{u6}, 0, kQ_{u8} ...$

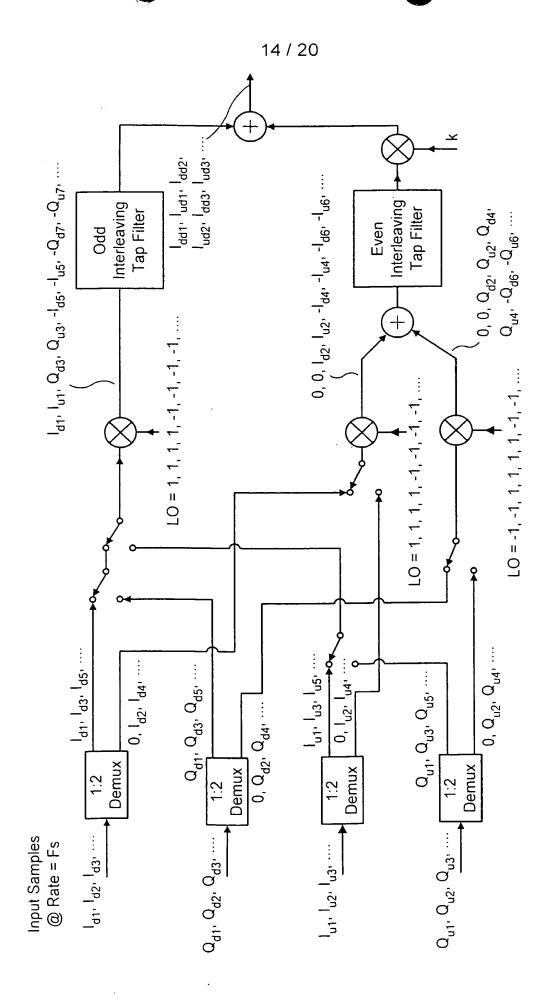
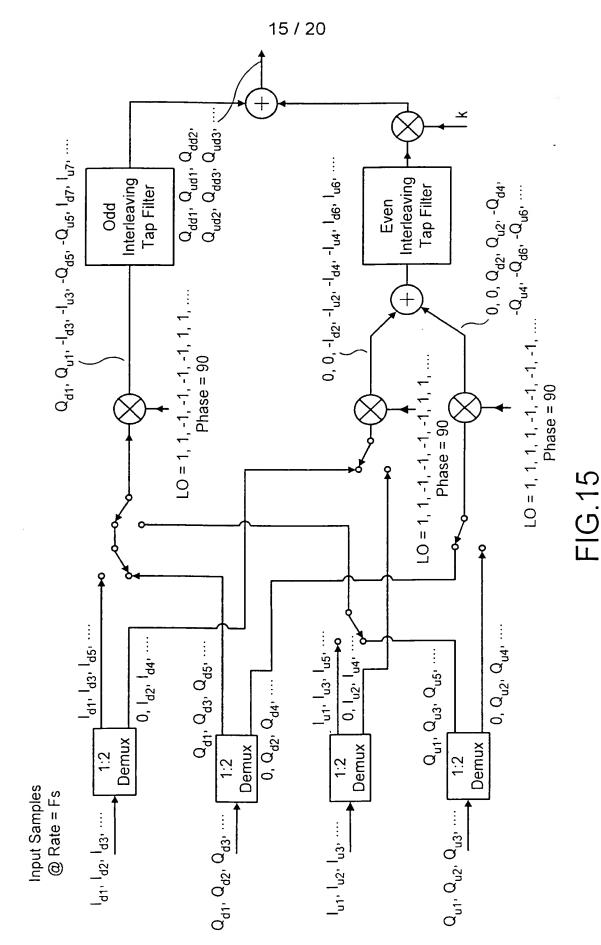
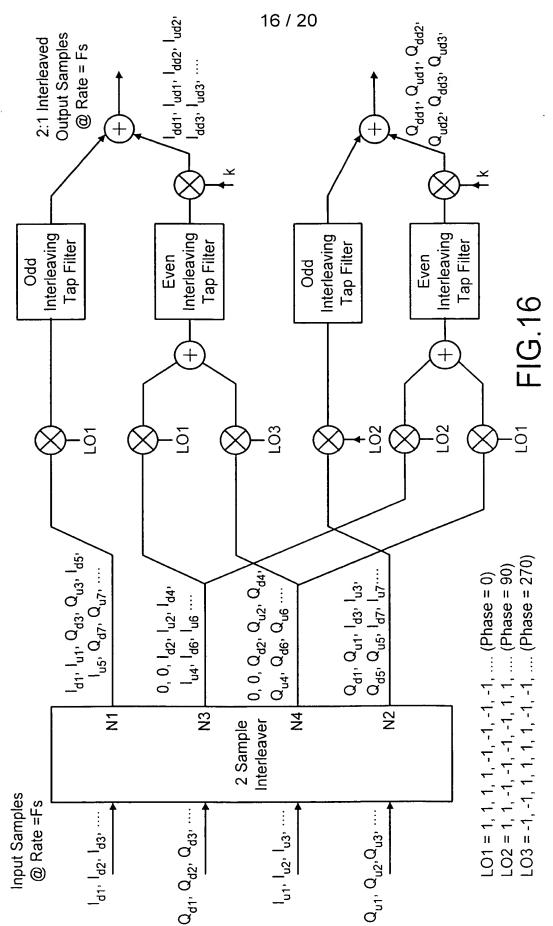


FIG.14

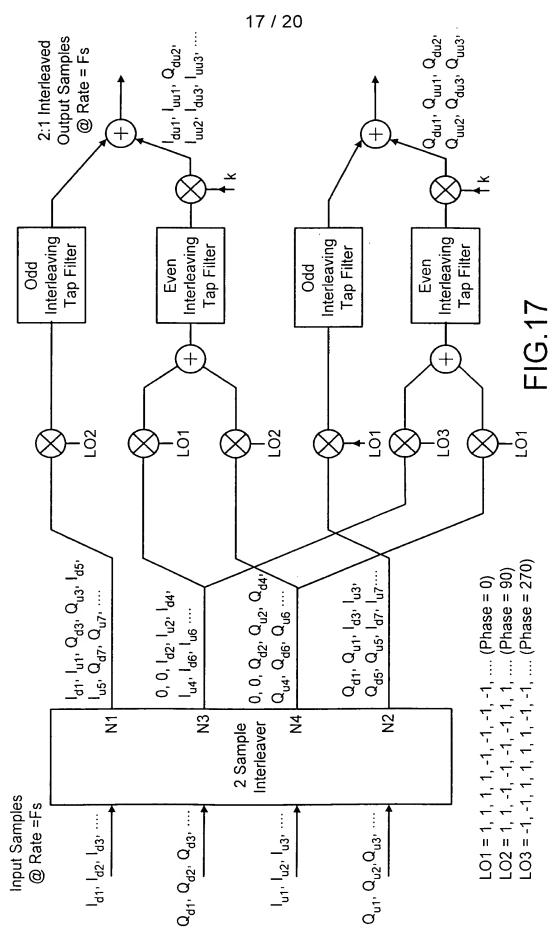
Simplified ICDC(B), I Channel Only



Simplified ICDC(B), Q Channel Only



Simplified ICDC(B), Combined I & Q Channels



Simplified ICUC(B), Combined I & Q Channels

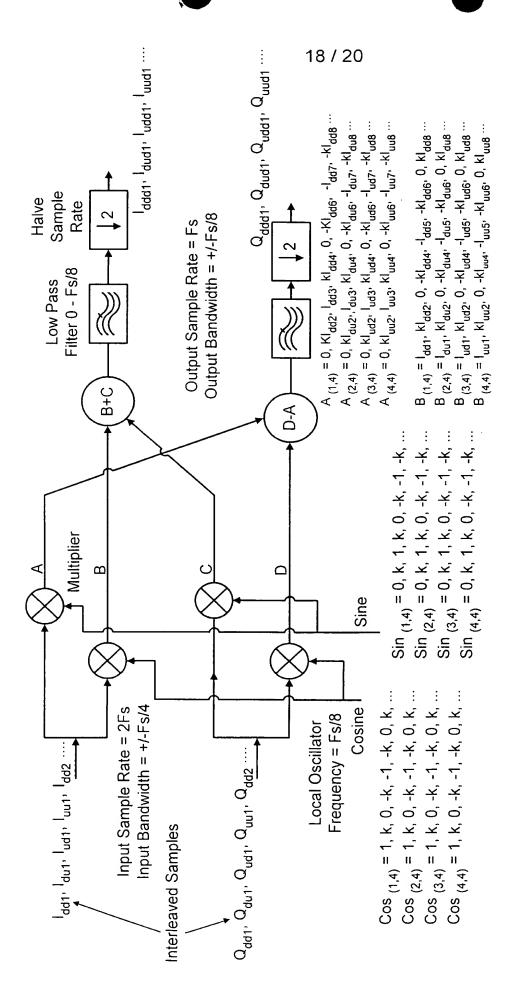


FIG.18

BASIC ICDC(C) ARCHITECTURE

 $C_{(1,4)} = 0, \ kQ_{dd2}, \ Q_{dd3}, \ kQ_{dd4}, \ 0, \ -kQ_{dd6}, \ -Q_{dd7}, \ -kQ_{dd8} \ \dots$ $C_{(2,4)} = 0, \ kQ_{du2}, \ Q_{du3}, \ kQ_{du4}, \ 0, \ -kQ_{du6}, \ -Q_{du7}, \ -kQ_{du8} \ \dots$ $C_{(3,4)} = 0, \ kQ_{ud2}, \ Q_{ud3}, \ kQ_{ud4}, \ 0, \ -kQ_{ud6}, \ -Q_{ud7}, \ -kQ_{uu8} \ \dots$ $C_{(4,4)} = 0, \ kQ_{uu2}, \ Q_{uu3}, \ kQ_{uu4}, \ 0, \ -kQ_{uu6}, \ -Q_{uu7}, \ -kQ_{uu8} \ \dots$ $D_{(1,4)} = Q_{dd1}, \ kQ_{dd2}, \ 0, \ -kQ_{dd4}, \ -Q_{dd5}, \ -kQ_{dd6}, \ 0, \ kQ_{du8} \ \dots$ $D_{(2,4)} = Q_{ud1}, \ kQ_{ud2}, \ 0, \ -kQ_{ud4}, \ -Q_{ud5}, \ -kQ_{ud6}, \ 0, \ kQ_{ud8} \ \dots$ $D_{(3,4)} = Q_{uu1}, \ kQ_{uu2}, \ 0, \ -kQ_{uu4}, \ -Q_{uu5}, \ -kQ_{ud6}, \ 0, \ kQ_{uu8} \ \dots$ $D_{(4,4)} = Q_{uu1}, \ kQ_{uu2}, \ 0, \ -kQ_{uu4}, \ -Q_{uu5}, \ -kQ_{uu6}, \ 0, \ kQ_{uu8} \ \dots$ $D_{(4,4)} = Q_{uu1}, \ kQ_{uu2}, \ 0, \ -kQ_{uu4}, \ -Q_{uu5}, \ -kQ_{uu6}, \ 0, \ kQ_{uu8} \ \dots$ $D_{(4,4)} = Q_{uu1}, \ kQ_{uu2}, \ 0, \ -kQ_{uu4}, \ -Q_{uu5}, \ -kQ_{uu6}, \ 0, \ kQ_{uu8} \ \dots$

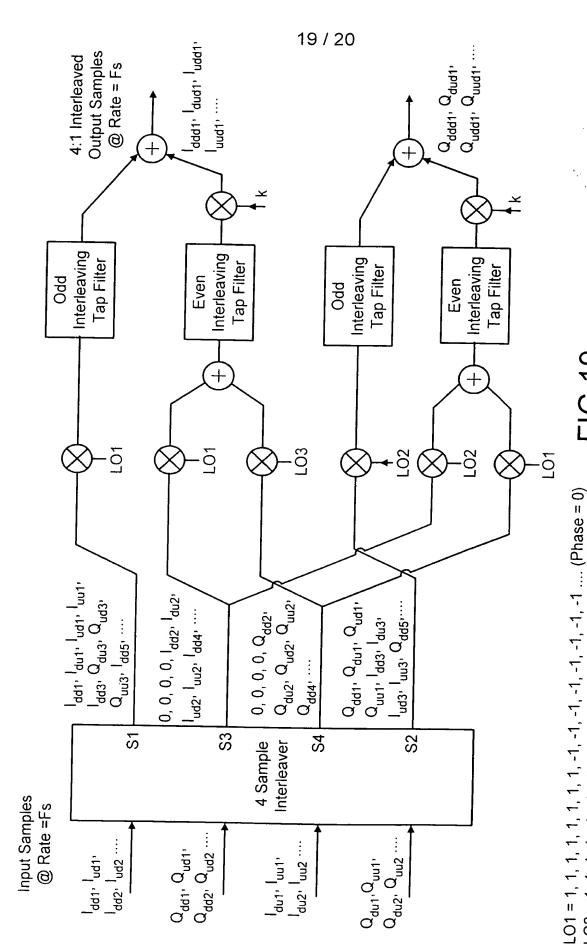
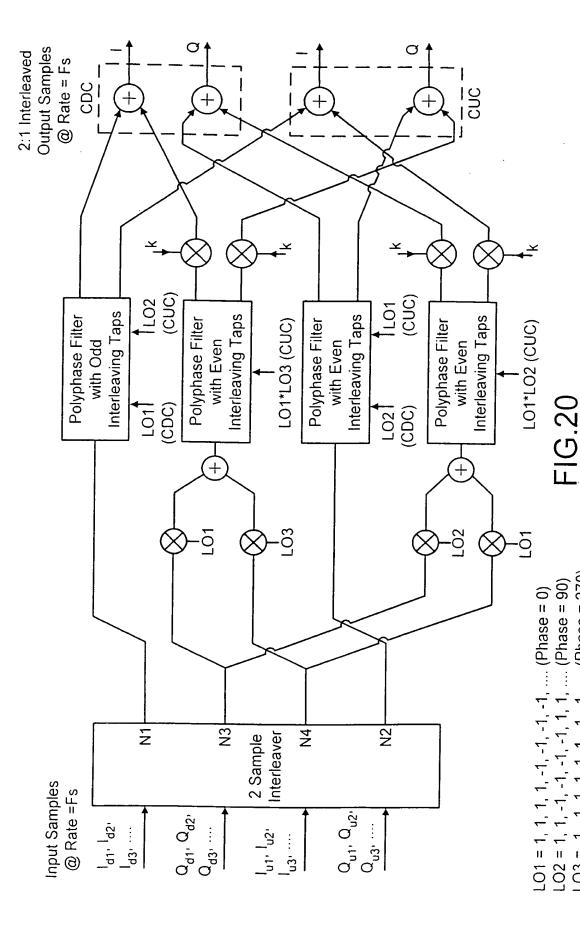


FIG. 19

l, -1, -1, -1, -1, -1, -1, 1, 1, 1, 1, 1 (Phase = 90)

L02 = 1, 1

Simplified ICDC(C), Combined I & Q Channels



Combined ICDCB) / ICUC(B) With Polyphase Filters

LO3 = -1, -1, 1, 1, 1, -1, -1, (Phase = 270)